

Amendments to the Claims:

Please replace all prior versions, and listings of claims in the application with the following listing of claims.

Listing of claims

Claim 1 (Currently Amended): Apparatus for use in a computer system, comprising:

a bus architecture;

a plurality of modules connected to the bus architecture; and

a centralized arbiter comprising:

a first arbiter means for controlling initiating transactions on the bus

architecture; and

a second arbiter means for controlling return transactions on the bus

architecture;

the first and second arbiter means operating independently of one another.

Claim 2 (Previously Presented): Apparatus as claimed in claim 1, wherein the bus architecture has separate read, write and transaction buses, and wherein the first arbiter means controls use of the write and transaction buses and the second arbiter means controls use of the read bus.

Claim 3 (original): A computer system comprising apparatus as claimed in claim 1.

Claim 4 (original): An integrated circuit comprising apparatus as claimed in claim 1.

Claim 5 (original): A graphics processing system comprising apparatus as claimed in claim 1.

Claim 6 (original): A games console comprising apparatus as claimed in claim 1.